SECTION 2. FORMS PTO/SB/08A and 08B (formerly Form PTO-1449)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Zeryck et al.

Attorney Docket:

1956/116

Serial No:

09/375,331

Art Group Unit:

2126

Date Filed:

August 17, 1999

Examiner Name:

Hoang, P.

Invention:

Computer Architecture Utilizing Layered Devices

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

| U.S. PATENT DOCUMENTS | | | | | |
|-----------------------|---------------------|-----------------|---------------|------------------|----------------|
| Examiner Initials | Reference Number | Document Number | Issue Date | Inventor | Class/Subclass |
| PH | AB | US 5,339,432 | Aug. 16, 1994 | Crick | 395/700 |
| PH | AC | US 5,432,941 | Jul. 11, 1995 | Crick et al. | 395/700 |
| PH | AD | US 5,608,651 | Mar. 4, 1997 | Leavy et al. | 364/514 |
| PH | AE | US 5,630,076 | May 13, 1997 | Saulpaugh et al. | 395/284 |
| PH | AF | US 5,732,282 | Mar. 24, 1998 | Provino et al. | 395/830 |
| PH | AG | US 5,761,411 | June 2, 1998 | Teague et al. | 395/184.01 |
| PH | AH | US 5,781,797 | Jul 14, 1998 | Crick et al. | 395/830 |
| рн | AI | US 5,784,615 | Jul. 21, 1998 | Lipe et al. | 395/681 |
| PH | AJ | US 6,032,217 | Feb. 29, 2000 | Arnott | 710/200 |
| PH | AK | US 6,041,363 | Mar. 21, 2000 | Schaffer | 709/301 |
| PH | AL | US 6,205,492 | Mar. 20. 2001 | Shaw et al. | 709/321 |
| PH | AM | US 6,401,223 | June 4, 2002 | DePenning | 714/42 |
| PH | AN | US Re38,134 | June 3, 2003 | Ross et al. | 710/20 |
| PH | AO | US 6,694,317 | Feb. 17, 2004 | Stakutis et al. | 707/10 |

Examiner Signature:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation *if not* in conformance and not considered. Include copy of this form with next communication to applicant.

Muschon

SECTION 2:

FORM PTO 1449 - MODIFIED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Zeryck et al.

Atty Dkt:

1956/116

Serial No:

09/375,331

Date: December 23, 1999

Date Filed:

August 17, 1999

Invention:

COMPUTER ARCHITECTURE UTILIZING LAYERED DEVICE DRIVERS

Assistant Commissioner for Patents Washington, DC 20231

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS' INFORMATION DISCLOSURE STATEMENT

U.S. Documents

Exam. Ref.

Document Number

Issue

Date

Name

Class

PH

Init.

AA

<u>No.</u>

5,802,364

Sep. 1, 1998

Senator et al.

395/681

Examiner:

Date Considered: 5

NOTE FOR EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance AND not considered. Include copy of this form with next communication to applicant. 105029 Page 4 of 6